IN THE CLAIMS:

No claim amendments are proposed by Applicants in this response.

1. (Previously Presented) A computer implemented method comprising:

requesting a first deferred procedure call for a first interrupt event associated with a source;

requesting at least one other different deferred procedure call for a second interrupt event associated with the source, wherein the first interrupt event comprises one type of event and the second interrupt event comprises another type of event;

assigning the first deferred procedure call and the at least one other deferred procedure call to a resource;

processing the first interrupt event with the first deferred procedure call; and

processing the second interrupt event with the at least one other deferred procedure call.

2. (Original) The method of claim 1, further comprising:

assigning the first deferred procedure call and the at least one other deferred procedure call to a resource comprising a processor exhibiting a single thread of execution; and

executing the first deferred procedure call and the at least one other deferred procedure call on the single thread.

3. (Original) The method of claim 1, further comprising:

assigning the first deferred procedure call and the at least one other deferred procedure call to a resource comprising a processor exhibiting a plurality of threads; and

executing the first deferred procedure call on one thread of the plurality of threads while executing the at least one other deferred procedure call on another thread of the plurality of threads.

4. (Original) The method of claim 1, further comprising:

assigning the first deferred procedure call to a resource comprising a first thread of a processor;

assigning the at least one other deferred procedure call to a resource comprising a second thread of the processor; and

executing the first deferred procedure call on the first thread while executing the at least one other deferred procedure call on the second thread.

5. (Original) The method of claim 1, further comprising:

assigning the first deferred procedure call and the at least one other deferred procedure call to a resource comprising a multi-processor system; and

executing the first deferred procedure call on one processor of the multi-processor system while executing the at least one other deferred procedure call on another processor of the multi-processor system.

6. (Original) The method of claim 1, further comprising:
assigning the first deferred procedure call to a resource comprising a first processor;
assigning the at least one other deferred procedure call to a resource comprising a second processor; and
executing the first deferred procedure call on the first processor while executing the at least one other deferred procedure call on the second processor.

7. (Previously Presented) The method of claim 1, further comprising
processing a third interrupt event associated with the source with the first deferred procedure call, the third interrupt event comprising a third type of event.

8. (Previously Presented) A computer implemented method comprising:
requesting a first deferred procedure call for a first interrupt event associated with a source;
requesting at least one other different deferred procedure call for a second interrupt event associated with the source, wherein the first interrupt event comprises one type of event and the second interrupt event comprises another type of event; and
processing the first interrupt event with the first deferred procedure call while processing the second interrupt event with the at least one other deferred procedure call.

9. (Original) The method of claim 8, further comprising:
executing the first deferred procedure call on a first thread of a processor; and
executing the at least one other deferred procedure call on a second thread of the processor.

10. (Original) The method of claim 8, further comprising:
executing the first deferred procedure call on a first processor; and
executing the at least one other deferred procedure call on a second processor.

11. (Previously Presented) The method of claim 8, further comprising
processing a third interrupt event associated with the source with the first deferred procedure call, the third interrupt event comprising a third type of event.

12. (Previously Presented) A driver comprising:
an interrupt handler to identify interrupt events associated with a source;
a first deferred procedure call, the first deferred procedure call to process a first type of the interrupt events; and
a second different deferred procedure call, the second deferred procedure call to process a second type of the interrupt events.

13. (Previously Presented) The driver of claim 12, the interrupt handler to assign the first and second deferred procedure calls to a resource for execution.

14. (Previously Presented) The driver of claim 12, the interrupt handler to assign the first deferred procedure call to a first resource for execution and the second deferred procedure call to a second resource for execution.

15. (Previously Presented) A computer system comprising:

a driver stored in a memory of the computer system, the driver including

an interrupt handler to identify interrupt events associated with a source;

a first deferred procedure call, the first deferred procedure call to process a first type of the interrupt events; and

a second different deferred procedure call, the second deferred procedure call to process a second type of the interrupt events;

and

a processor to execute the ~~the~~ first and second deferred procedure calls.

16. (Previously Presented) The computer system of claim 15, the interrupt handler to assign the first and second deferred procedure calls to a single thread exhibited by the processor for execution.

17. (Previously Presented) The computer system of claim 15, the interrupt handler to assign the first deferred procedure call to one thread of the processor and the second deferred procedure call to a second thread of the processor for execution.

18. (Previously Presented) The computer system of claim 15, the interrupt handler to assign the first deferred procedure call to the processor and the second deferred procedure call to a second processor for execution.

19. (Previously Presented) The computer system of claim 15, wherein the source comprises a peripheral device coupled with the computer system.

20. (Previously Presented) An article of manufacture comprising:

a machine accessible medium, the machine accessible medium providing instructions that, when executed by a machine, cause the machine to:

request a first deferred procedure call for a first interrupt event associated with a source;

request at least one other different deferred procedure call for a second interrupt event associated with the source, wherein the first interrupt event comprises one type of event and the second interrupt event comprises another type of event;

assign the first deferred procedure call and the at least one other deferred procedure call to a resource;

process the first interrupt event with the first deferred procedure call; and

process the second interrupt event with the at least one other deferred procedure call.

21. (Original) The article of claim 20, wherein the instructions, when executed, further cause the machine to:

assign the first deferred procedure call and the at least one other deferred procedure call to a resource comprising a processor exhibiting a single thread of execution; and

execute the first deferred procedure call and the at least one other deferred procedure call on the single thread.

22. (Original) The article of claim 20, wherein the instructions, when executed, further cause the machine to:

assign the first deferred procedure call and the at least one other deferred procedure call to a resource comprising a processor exhibiting a plurality of threads; and

execute the first deferred procedure call on one thread of the plurality of threads while executing the at least one other deferred procedure call on another thread of the plurality of threads.

23. (Previously Presented) The article of claim 20, wherein the instructions, when executed, further cause the machine to:

assign the first deferred procedure call to a resource comprising a first thread of a processor;

assign the at least one other deferred procedure call to a resource comprising a second thread of the processor; and

execute the first deferred procedure call on the first thread while executing the at least one other deferred procedure call on the second thread.

24. (Original) The article of claim 20, wherein the instructions, when executed, further cause the machine to:

assign the first deferred procedure call and the at least one other deferred procedure call to a resource comprising a multi-processor system; and

execute the first deferred procedure call on one processor of the multi-processor system while executing the at least one other deferred procedure call on another processor of the multi-processor system.

25. (Original) The article of claim 20, wherein the instructions, when executed, further cause the machine to:

assign the first deferred procedure call to a resource comprising a first processor;

assign the at least one other deferred procedure call to a resource comprising a second processor; and

execute the first deferred procedure call on the first processor while executing the at least one other deferred procedure call on the second processor.

26. (Previously Presented) The article of claim 20, wherein the instructions, when executed, further cause the machine to process a third interrupt event associated with the source with the first deferred procedure call, the third interrupt event comprising a third type of event.

27. (Previously Presented) An article of manufacture comprising:

a machine accessible medium, the machine accessible medium providing instructions that, when executed by a machine, cause the machine to:

request a first deferred procedure call for a first interrupt event associated with a source;

request at least one other different deferred procedure call for a second interrupt event associated with the source, wherein the first interrupt event comprises one type of event and the second interrupt event comprises another type of event; and

process the first interrupt event with the first deferred procedure call while processing the second interrupt event with the at least one other deferred procedure call.

28. (Original) The article of claim 27, wherein the instructions, when executed, further cause the machine to:

execute the first deferred procedure call on a first thread of a processor; and

execute the at least one other deferred procedure call on a second thread of the processor.

29. (Original) The article of claim 27, wherein the instructions, when executed, further cause the machine to:

execute the first deferred procedure call on a first processor; and

execute the at least one other deferred procedure call on a second processor.

30. (Previously Presented) The article of claim 27, wherein the instructions, when executed, further cause the machine to process a third interrupt event associated with the source with the first deferred procedure call, the third interrupt event comprising a third type of event.

31. (Previously Presented) The method of claim 1, wherein the source comprises a peripheral device of a computer system.

32. (Previously Presented) The method of claim 8, wherein the source comprises a peripheral device of a computer system.

33. (Previously Presented) The driver of claim 12, wherein the source comprises a peripheral device of a computer system.

34. (Previously Presented) The article of manufacture of claim 20, wherein the source comprises a peripheral device of a computer system.

35. (Previously Presented) The article of manufacture of claim 27, wherein the source comprises a peripheral device of a computer system.